

Electronic Acknowledgement Receipt

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Title of Invention:	RISC MICROPROCESSOR ARCHITECTURE IMPLEMENTING MULTIPLE TYPED REGISTER SETS
First Named Inventor/Applicant Name:	Sanjiv Garg
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File Listing:

Document Number	Document Description	File Name	File Size(Bytes)/ Message Digest	Multi Part /.zip	Pages (if appl.)
1		32221970007POA373BCCA.pdf	187915 3e6dd2b9271017af9eba3a5bf048097824329ab5	yes	5

	Multipart Description/PDF files in .zip description		
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	Assignee showing of ownership per 37 CFR 3.73(b).	3	4
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